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(Not for submission under 37 CFR 1.99)

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| Application Number | 10577288 |
| Filing Date | 2007-01-15 |
| First Named Inventor | Michael Goessel |
| Art Unit | 2117 |
| Examiner Name | Daniel F. McMahon |
| Attorney Docket Number | I431.135.101/FIN516PCT/US |

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|--|---|--|--------------------------|
| | 1 | M. Goessel et al., "Signature Analysis for Identifying Failing Vectors", XP 002317738, 4 pps. | <input type="checkbox"/> |
| | 2 | M. Goessel et al., "A Parity-Preserving Multi-Input Signature Analyzer and its Application for Concurrent Checking and BIST", Journal of Electronic Testing, Vol. 8, Nr. 2, March 27, 1995, pps 165-176 (XP000598709). | <input type="checkbox"/> |
| | 3 | Y. Wu et al., "Scan-Based BIST Fault Diagnosis", IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems, Vol. 18, No. 2, Feb. 1999, pps. 203-211. | <input type="checkbox"/> |
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